30V N-CHANNEL ENHANCEMENT MODE MOSFET 2.5V GATE DRIVE

SUMMARY

 $V_{(BR)DSS}$ =30V : $R_{DS}(_{on})$ =0.025 Ω ; I_{D} = 8.9A

DESCRIPTION

This new generation of Trench MOSFETs from Zetex utilizes a unique structure that combines the benefits of low on-resistance with fast switching speed. This makes them ideal for high efficiency, low voltage, power management applications.



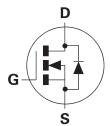
SO8

FEATURES

- · Low on-resistance
- · Fast switching speed
- Low threshold
- · Low gate drive
- Low profile SOIC package

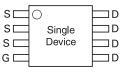
APPLICATIONS

- DC DC converters
- Power management functions
- · Disconnect switches
- Motor control



ORDERING INFORMATION

DEVICE	REEL SIZE	TAPE WIDTH	QUANTITY PER REEL
ZXMN3B04N8TA	7″	12mm	500 units
ZXMN3B04N8TC	13"	12mm	2500 units



Top View

DEVICE MARKING

 ZXMN 3B04



ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-source voltage	V _{DSS}	30	V
Gate source voltage	V _{GS}	±12	V
Continuous drain current @ V _{GS} =4.5V; T _A =25°C ^(b)	I _D	8.9	А
@ V _{GS} =4.5V; T _A =70°C ^(b)		7.3	Α
@ V _{GS} =4.5V; T _A =25°C ^(a)		7.2	Α
Pulsed drain current (c)	I _{DM}	45	А
Continuous source current (body diode) (b)	Is	4.5	А
Pulsed source current (body diode) (c)	I _{SM}	45	А
Power dissipation at T _A =25°C ^(a)	P _D	2	W
Linear derating factor		16	mW/°C
Power dissipation at T _A =25°C ^(b)	P_{D}	3	W
Linear derating factor		24	mW/°C
Operating and storage temperature range	T _j :T _{stg}	-55 to +150	°C

THERMAL RESISTANCE

PARAMETER	SYMBOL	VALUE	UNIT
Junction to ambient ^(a)	$R_{\Theta JA}$	62.5	°C/W
Junction to ambient ^(b)	$R_{\Theta JA}$	41.4	°C/W

NOTES

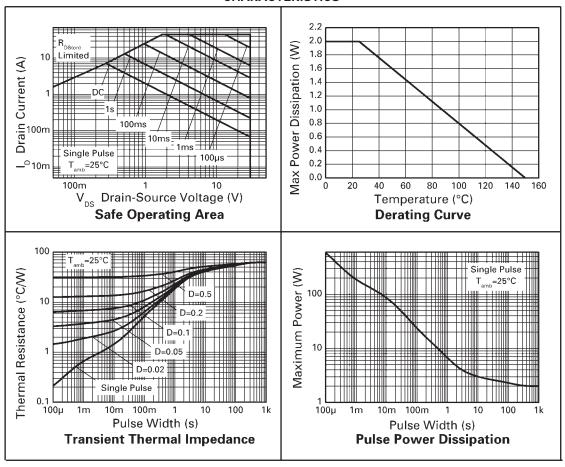


⁽a) For a device surface mounted on 50mm x 50mm FR4 PCB with high coverage of single sided 2oz copper, in still air conditions.

(b) For a device surface mounted on FR4 PCB measured at t ≤ 10 sec.

⁽c) Repetitive rating - 25mm x 25mm FR4 PCB, D=0.02, pulse width 300µs - pulse width limited by maximum junction temperature.

CHARACTERISTICS





ELECTRICAL CHARACTERISTICS (at T_{amb} = 25°C unless otherwise stated)

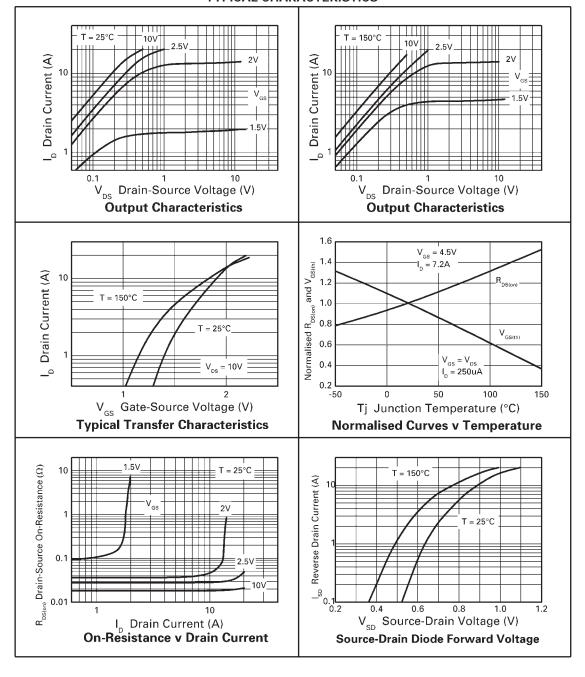
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	CONDITIONS
STATIC	<u> </u>			•		
Drain-source breakdown voltage	V _{(BR)DSS}	30			V	I _D =250μA, V _{GS} =0V
Zero gate voltage drain current	I _{DSS}			0.5	μΑ	V _{DS} =30V, V _{GS} =0V
Gate-body leakage	I _{GSS}			100	nA	$V_{GS} = \pm 12V$, $V_{DS} = 0V$
Gate-source threshold voltage	V _{GS(th)}	0.7			V	I _D =250μA, V _{DS} = V _{GS}
Static drain-source on-state	R _{DS(on)}		0.021	0.025	Ω	V _{GS} =4.5V, I _D =7.2A
resistance ⁽¹⁾			0.028	0.040 Ω		$V_{GS} = 2.5V, I_D = 5.7A$
Forward transconductance (1) (3)	9 _{fs}		24		S	V _{DS} =15V,I _D =7.2A
DYNAMIC (3)						
Input capacitance	C _{iss}		2480		pF	V 15V V 0V
Output capacitance	C _{oss}		318		pF	V _{DS} =15V, V _{GS} =0V, f=1MHz
Reverse transfer capacitance	C _{rss}		184		pF	1 - 1101112
SWITCHING ^{(2) (3)}						
Turn-on delay time	t _{d(on)}		9		ns	V 15V V 4.5V
Rise time	t _r		11.5		ns	V _{DD} =15V, V _{GS} =4.5V I _D =1A
Turn-off delay time	t _{d(off)}		40		ns	$R_{G} = 6.0\Omega$
Fall time	t _f		16.6		ns	G * * *
Total gate charge	Q_g		23.1		nC	V _{DS} =15V,V _{GS} =4.5V,
Gate-source charge	Q _{gs}		4.9		nC	I _D =7.2A
Gate-drain charge	Q_{gd}		6.2		nC	10-7.27
SOURCE-DRAIN DIODE	•					
Diode forward voltage ⁽¹⁾	V_{SD}		0.85	0.95	V	T _J =25°C, I _S =8A,
						V _{GS} =0V
Reverse recovery time ⁽³⁾	t _{rr}		17.9		ns	$T_J = 25^{\circ}C, I_F = 3.2A,$
Reverse recovery charge ⁽³⁾	Q _{rr}		10		nC	di/dt= 100A/μs

NOTES

- (1) Measured under pulsed conditions. Pulse width $\leq 300 \mu s$; duty cycle $\leq\!2\%.$
- (2) Switching characteristics are independent of operating junction temperature.
- (3) For design aid only, not subject to production testing.

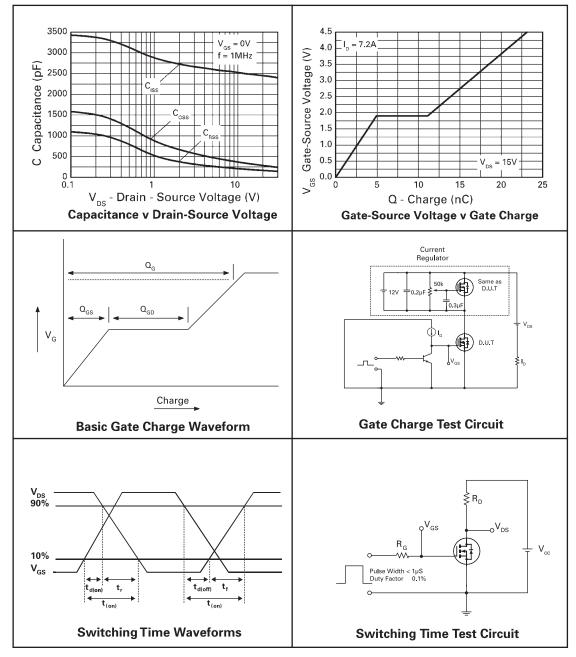


TYPICAL CHARACTERISTICS



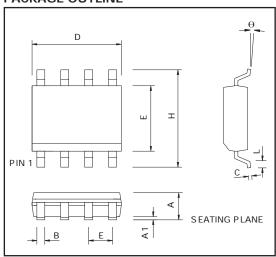
ZETEX SEMICONDUCTORS

TYPICAL CHARACTERISTICS





PACKAGE OUTLINE



Controlling dimensions are in inches. Approximate conversions are given in millimeters

PACKAGE DIMENSIONS

DIM	Inc	hes	Millin	neters	DIM	Inc	Inches		Millimeters	
DIIVI	Min	Max	Min	Max	DIIVI	Min	Max	Min	Max	
Α	0.053	0.069	1.35	1.75	е	0.050 BSC		1.27 BSC		
A1	0.004	0.010	0.10	0.25	b	0.013	0.020	0.33	0.51	
D	0.189	0.197	4.80	5.00	С	0.008	0.010	0.19	0.25	
Н	0.228	0.244	5.80	6.20	θ	0°	8°	0°	8°	
Е	0.150	0.157	3.80	4.00	h	0.010	0.020	0.25	0.50	
L	0.016	0.050	0.40	1.27						

© Zetex Semiconductors plc 2004

Europe	Americas	Asia Pacific	Corporate Headquarters
Zetex GmbH	Zetex Inc	Zetex (Asia) Ltd	Zetex plc
Streitfeldstraße 19	700 Veterans Memorial Hwy	3701-04 Metroplaza Tower 1	Lansdowne Road, Chadderton
D-81673 München	Hauppauge, NY 11788	Hing Fong Road, Kwai Fong	Oldham, OL9 9TY
Germany	USA	Hong Kong	United Kingdom
Telefon: (49) 89 45 49 49 0	Telephone: (1) 631 360 2222	Telephone: (852) 26100 611	Telephone (44) 161 622 4444
Fax: (49) 89 45 49 49	Fax: (1) 631 360 8222	Fax: (852) 24250 494	Fax: (44) 161 622 4446
europe.sales@zetex.com	usa.sales@zetex.com	asia.sales@zetex.com	hq@zetex.com

These offices are supported by agents and distributors in major countries world-wide.

This publication is issued to provide outline information only which (unless agreed by the Company in writing) may not be used, applied or reproduced for any purpose or form part of any order or contract or be regarded as a representation relating to the products or services concerned. The Company reserves the right to alter without notice the specification, design, price or conditions of supply of any product or service.

For the latest product information, log on to www.zetex.com

ZETEX SEMICONDUCTORS